

	Type	L #	Hits	Search Text
1	BRS	L1	642	custom\$4 near5 floor\$3
2	BRS	L2	332	custom\$4 near5 floor\$3
3	BRS	L3	145	2 and ( \$computer or \$processor or cpu or ( data adj base\$1 ) or database\$1 )
4	BRS	L4	200	1 and ( \$computer or \$processor or cpu or ( data adj base\$1 ) or database\$1 )
5	BRS	L5	58	installation adj pattern
6	BRS	L6	70	installation adj pattern
7	BRS	L7	1292	simulat\$4 near5 wood
8	BRS	L8	106	7 and ( \$computer or \$processor or cpu or ( data adj base\$1 ) or database\$1 )
9	BRS	L9	659	simulat\$4 near5 wood
10	BRS	L10	31	9 and ( \$computer or \$processor or cpu or ( data adj base\$1 ) or database\$1 )

	DBs	Time Stamp	Error Com m e n t s	E r r o r s
1	USPAT	2003/06/26 09:05		0
2	US-PGPUB ; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 09:05		0
3	US-PGPUB ; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 09:07		0
4	USPAT	2003/06/26 09:10		0
5	USPAT	2003/06/26 09:09		0
6	US-PGPUB ; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 09:10		0
7	USPAT	2003/06/26 09:11		0
8	USPAT	2003/06/26 09:11		0
9	US-PGPUB ; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 09:11		0
10	US-PGPUB ; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 09:12		0